

FIG. 1

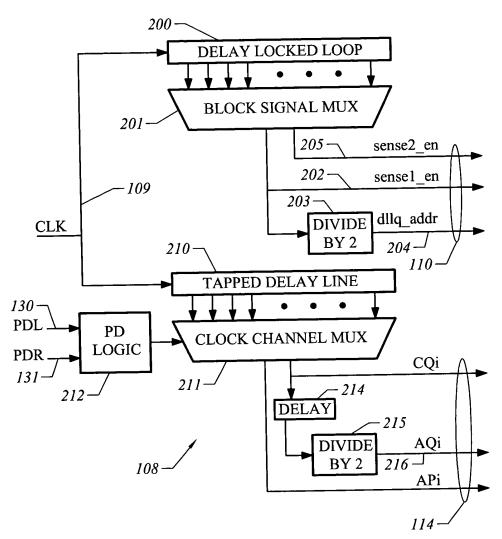
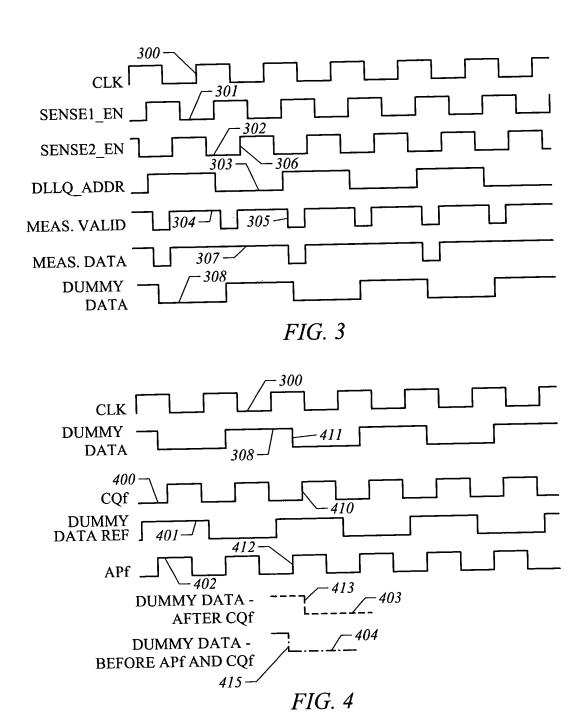


FIG. 2



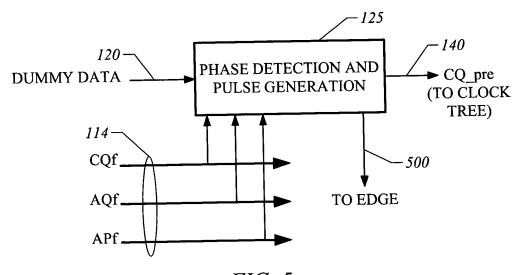


FIG. 5

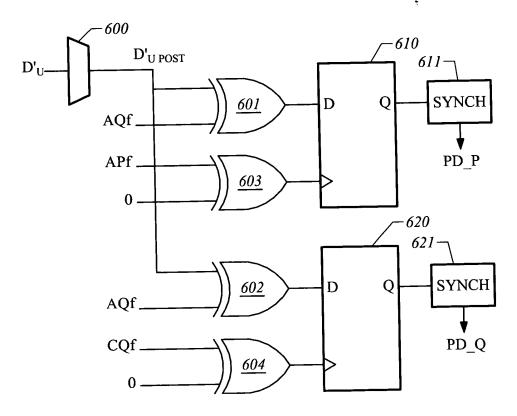
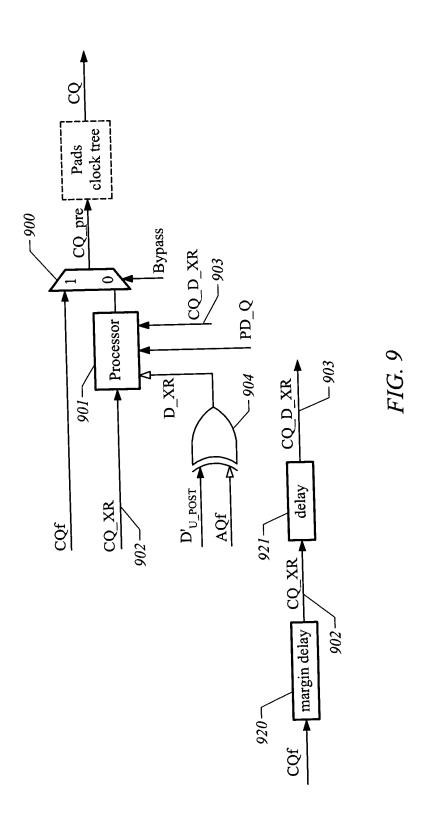


FIG. 6

	<u>–</u> ,												
	Decision (pdraw<2:0>=PD_P,PD_Q,1)	Increase the delay (001)	Increase the delay (001)	Keep the delay constant (011)	Decrease the delay (111))	Decision	Increase the delay	Increase the delay	Decrease the delay	(Only if the request is repeated for n cycles n=k* #(scanned hanks in one quadrant)	Keep the delay constant
	PD_Q	0	0	1	1	-	aw_1<1/				,	O) rycles n	6
_	PD_P	0	0	0	1		>, pdraw	6		(10)	3		
D' transition					{	FIG. 7	puraw_u< draw_u<2	PDL<1:0>	×	Increase (01)	,	Decrease	r cases
APF CQF CO			-			703	PDX<1>=AND(pdraw_u<2>, pdraw_1<2>)	PDR<1:0>	Increase (01)	×	3	Decrease (10) Decrease (10)	All other cases
700	<u> </u>	70.	9	-		7%							

FIG. 8



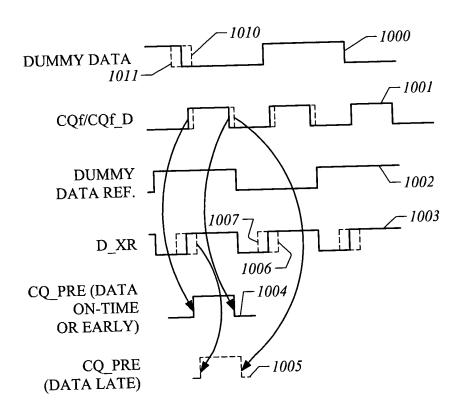


FIG. 10

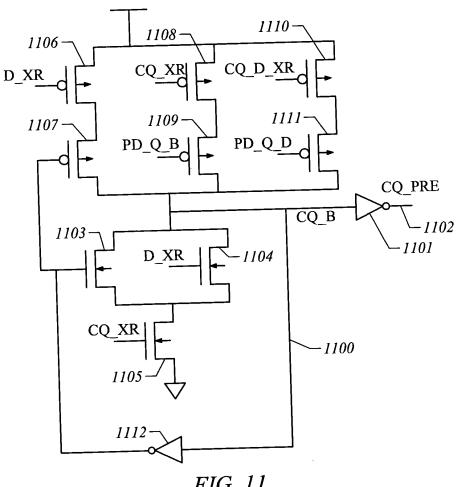
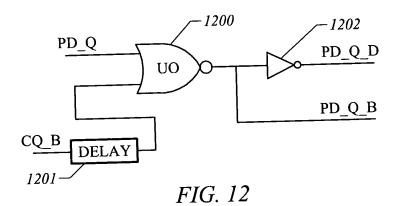
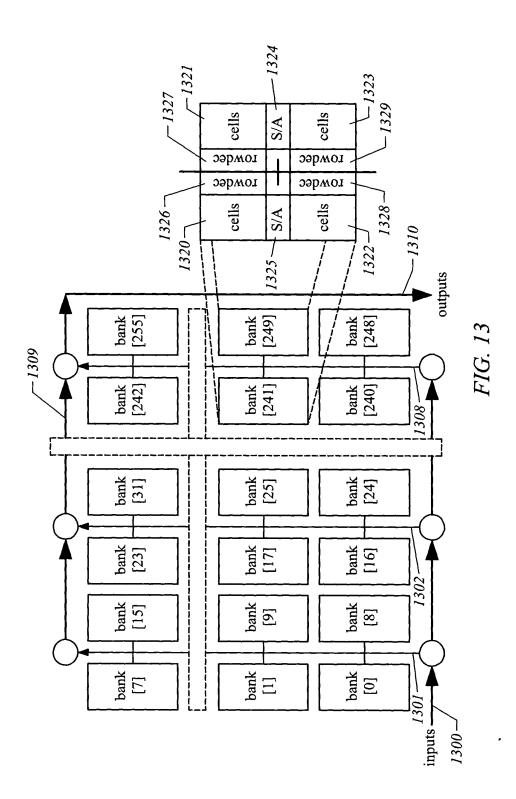


FIG. 11





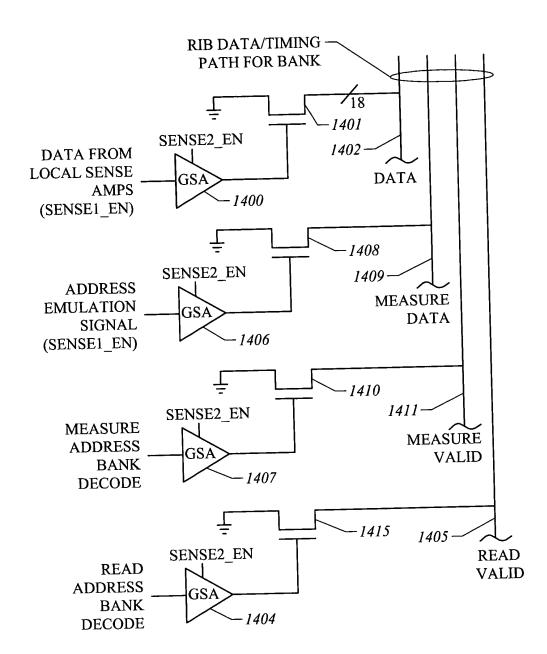
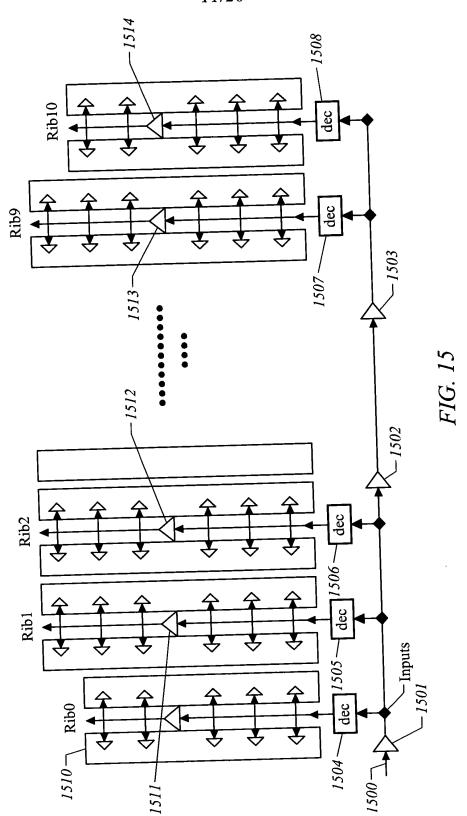
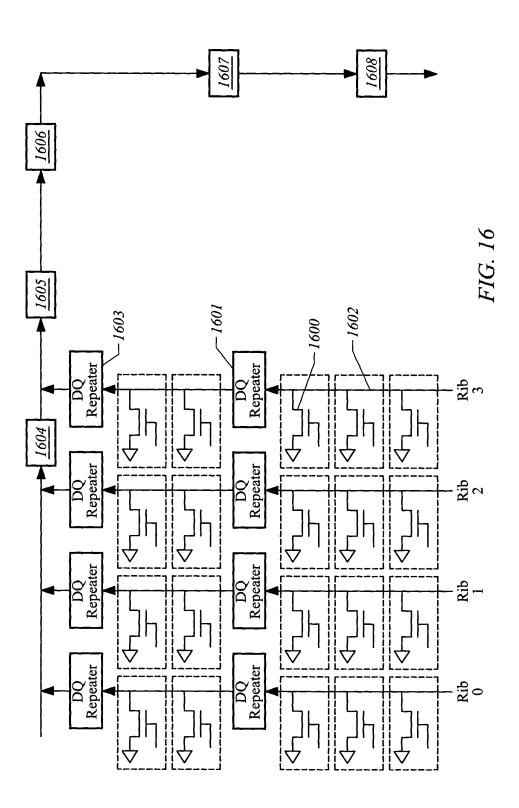
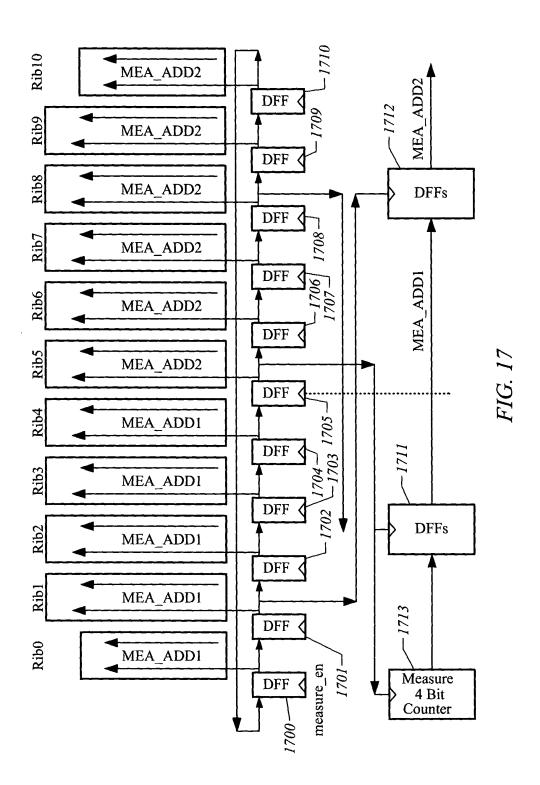


FIG. 14







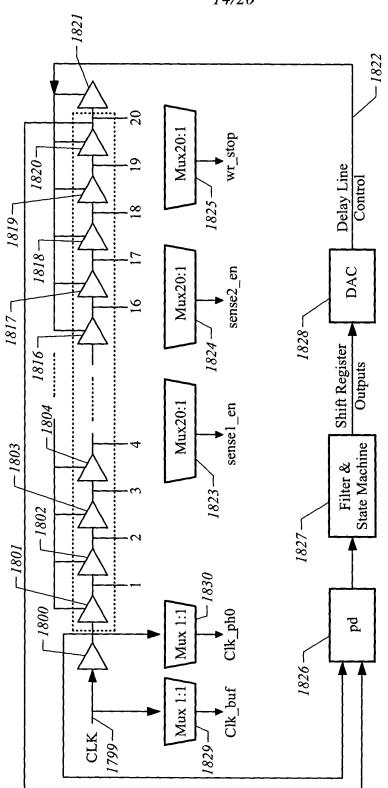


FIG. 18

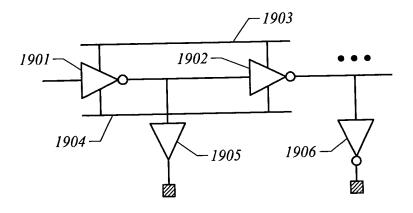


FIG. 19

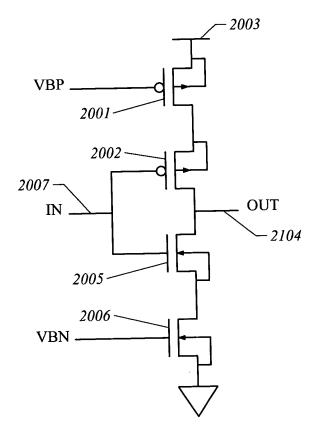


FIG. 20

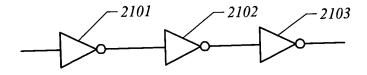


FIG. 21

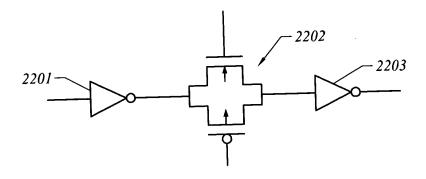
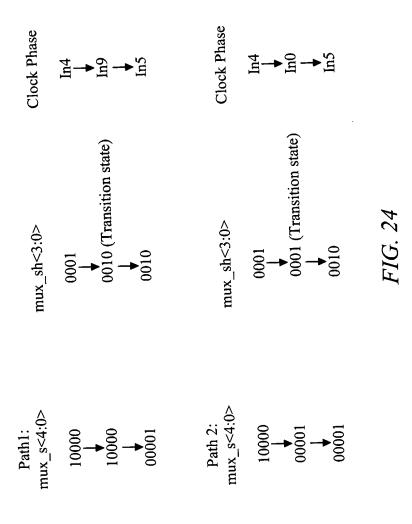


FIG. 22

	Co	Lowentrol	Higher Control Bits mux sh<3:0>							
	mux	_sp<	mux_shp<3:0>							
4	3	2	1	0	<u> </u>					
	X	X	X	X	0	0	0	0		
X 0	0	0	0	0	X	X	X	X		
0	0	0	0	1	0	0	0	1		
0	0	0	1	0	0	0	0	1		
0	0	1	0	0	0	0	0	1		
0	1	0	0	0	0	0	0	1		
1	0	0	0	0	0	0	0	1		
0	0	0	0	1_	0	0	1	0		
0	0	0	1	0	0	0	1	0		
0	0	1	0	0	0	0	1	0		
0	1	0	0	0	0	0	1	0		
1	0	0	0	0	0	0	1	0		
0	0	0	0_	1	0	1	0	0		
0	0	0	1	0	0	1	0	0		
0	0	1	0	0	0	1	0	0		
0	1	0	0	0	0	1	0	0		
1	0	0	0	0	0	1	0	0		
0	0	0	0	1	1	0	0	0		
0	0	0	1	0	1	0	0	0		
0	0	1	0	0	1	0	0	0		
0	1	0	0	0	1	0	0	0		
1	0	0	0	0	1	0	0	0		

cqf, aqf apf
Clock phase
$\frac{z}{z}$
Z
In0
In1
In2
In3
In4
In5
In6
In7
In8
In9

In10
In11
In12
In13
In14
In15
In16
In17
In18
In19



(4) C 1 60 16

	Co mu	Lowentrol x_s<	Bits 4:0>	Higher Control Bits mux_sh<3:0> mux_shp<3:0>						
4	3	2	1	0	3 2 1 0					
X	X	X	X	X	0	0	0	0		
0	0	0	0	0	X	X	X	X		
	Ť			_						
0	0	0	0	1	0	0	0	1		
0	0	0	1	0	0	0	0	1		
0	0	1	0	0	0	0	0	1		
0	1	0	0	0	0	0	0	1		
1	0	0	0	0	0	0	0	1		
1	0	0	0	0	0	0	1	0		
0	1	0	0	0	0	0	1	0		
0	0	1	0	0	0	0	1	0		
0	0	0	1	0	0	0	1	0		
0	0	0	0	1	0	0	1	0		
0	0	0	0	1	0	1	0	0		
0	0	0	1	0	0	1	0	0		
0	0	1	0	0	0	1	0	0		
0	1	0	0	0	0	1	0	0		
1	0	0	0	0	0	1	0	0		
1	0	0	0	0	1	0	0	0		
0	1	0	0	0	1	0	0	0		
0	0	1	0	0	1	0	0	0		
0	0	0	1	0	1	0	0	0		
0	0	0	0	1	1	0	0	0		

cqf, aqf
apf
-
Clock phase
Z
Z Z

In0
In1
In2
In3
In4
In5
In6
In7
In8
In9
In10
In11
In12
In13
In14
In15
In16
In17
In18
In19

FIG. 25

